



CAP-TEE

Capability Architectures in Trusted Execution



UNIVERSITY OF
BIRMINGHAM

CHERI & Trusted Execution Environments

Jennifer Jackson – University of Birmingham
In collaboration with KU Leuven

Our project is funded by the Digital Security by Design (DSbD) Programme delivered by UKRI to support the DSbD ecosystem

Jennifer Jackson, David Oswald, Mark Ryan, Mihai Ordean, Richard Thomas, Flavio Garcia

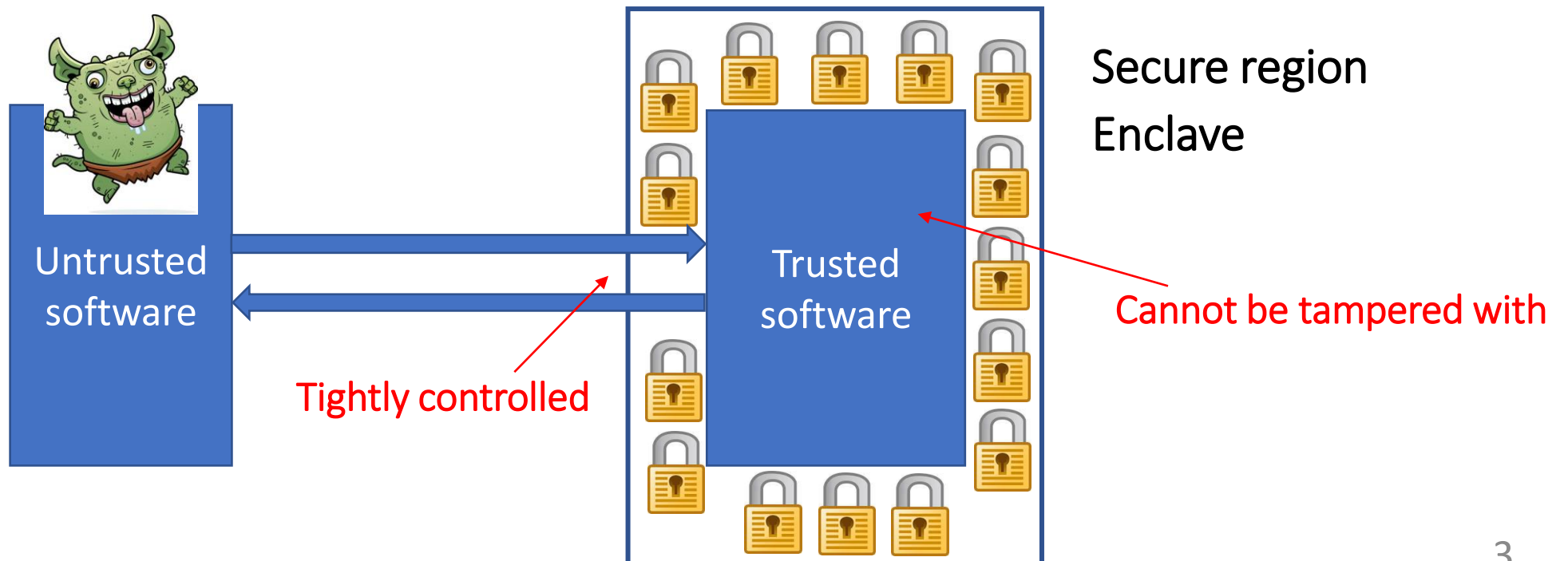
Introduction

- Trusted execution and capabilities
- CHERI-based prototype TEE.
- Object capabilities and domain switching
- Demo

TEE - Trusted Execution Environment and Capabilities

- **Trusted Execution Environments (TEEs)**

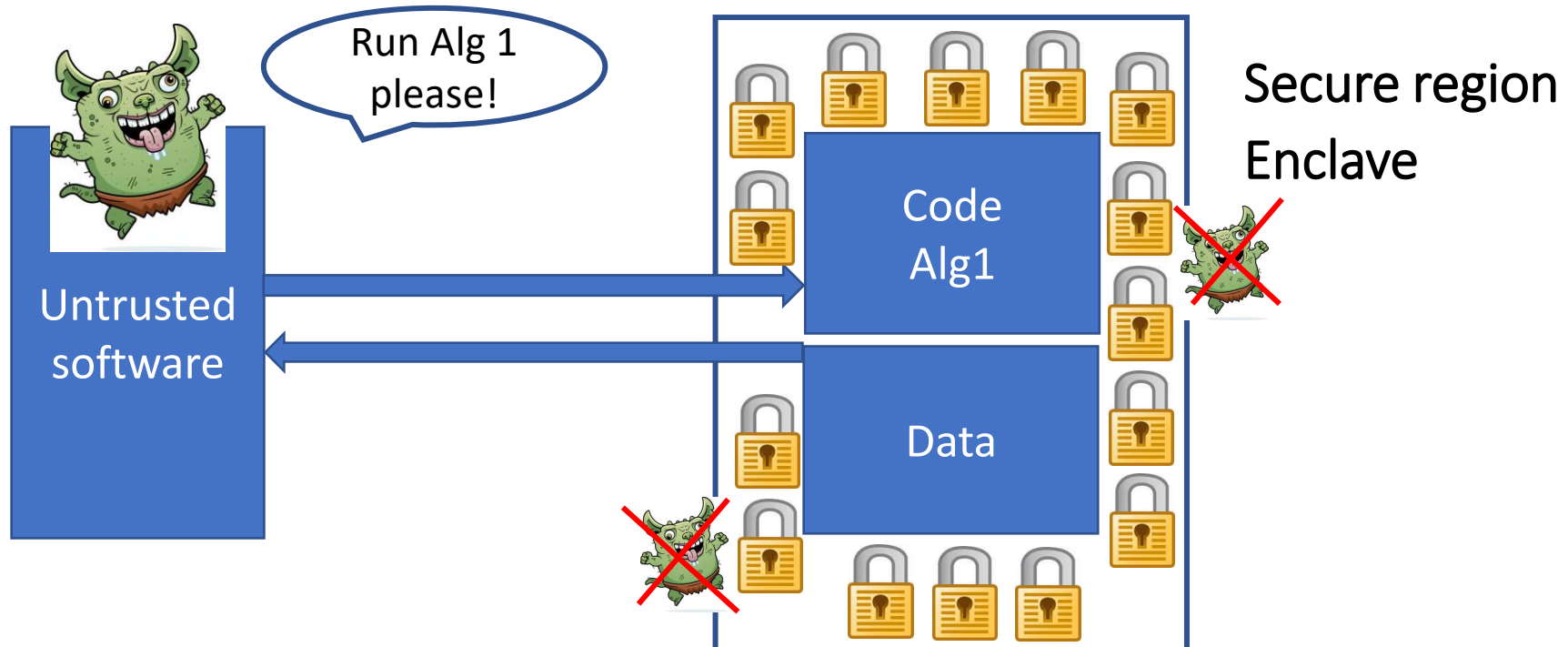
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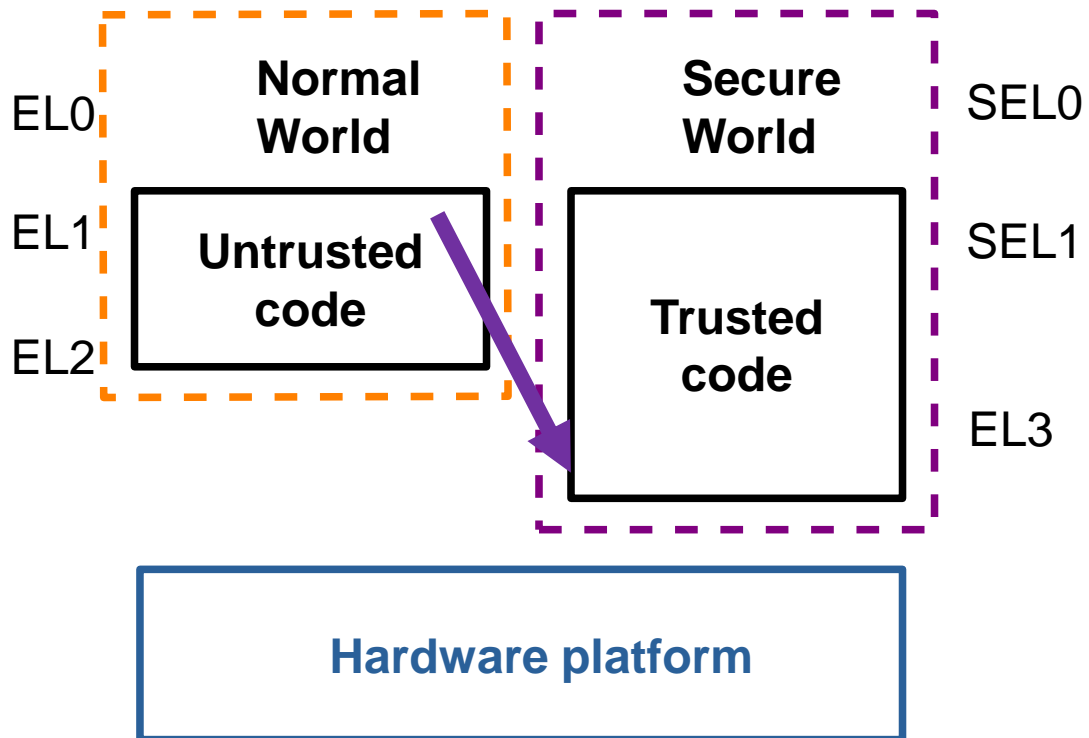
TEE - Trusted Execution Environment and Capabilities

Arm Trustzone

Intel SGX Application

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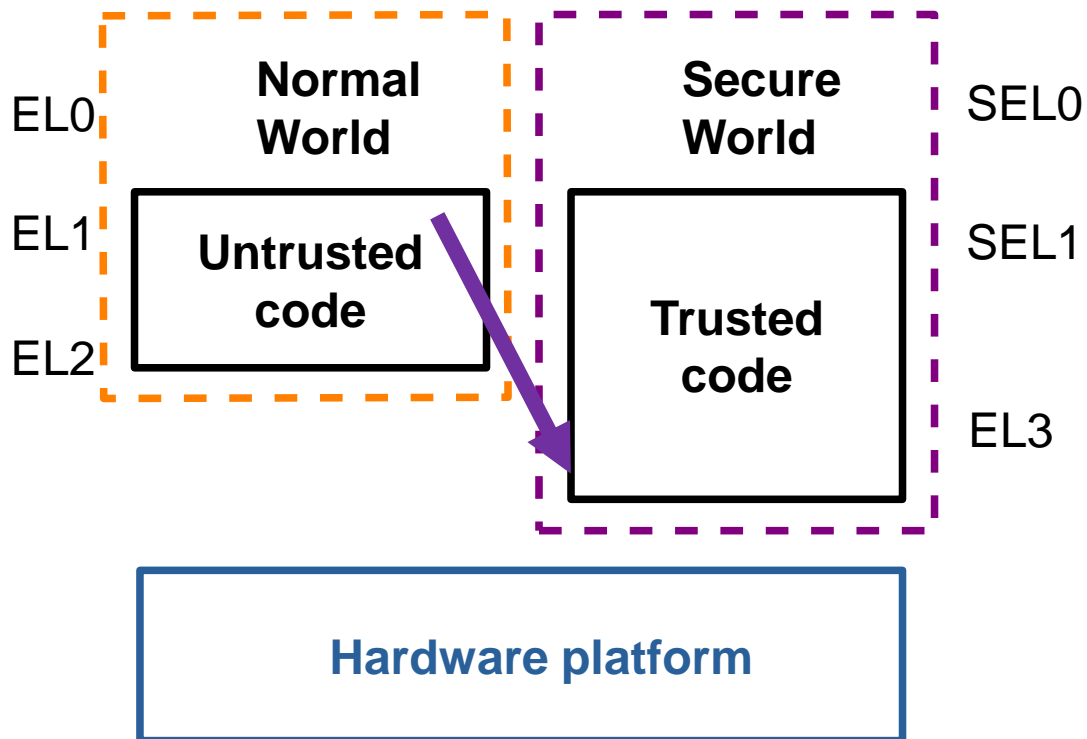
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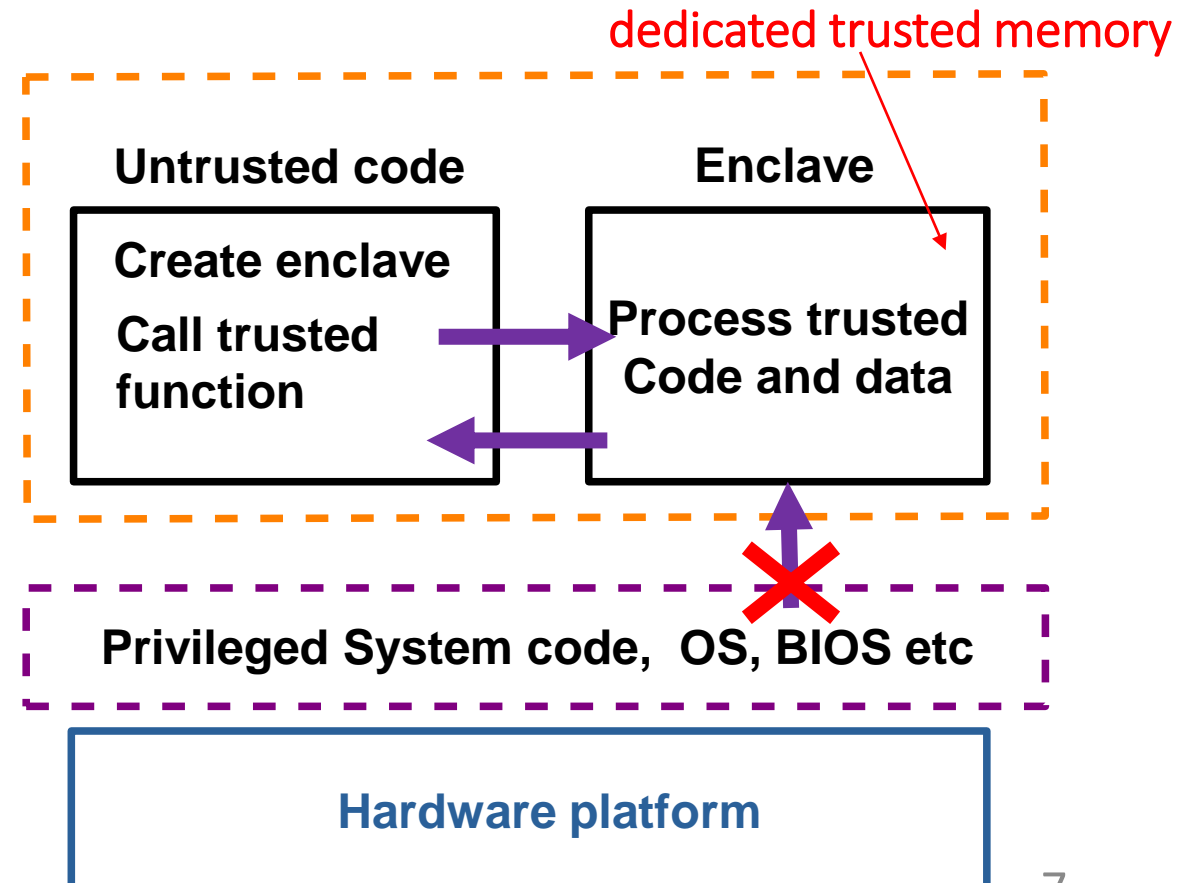
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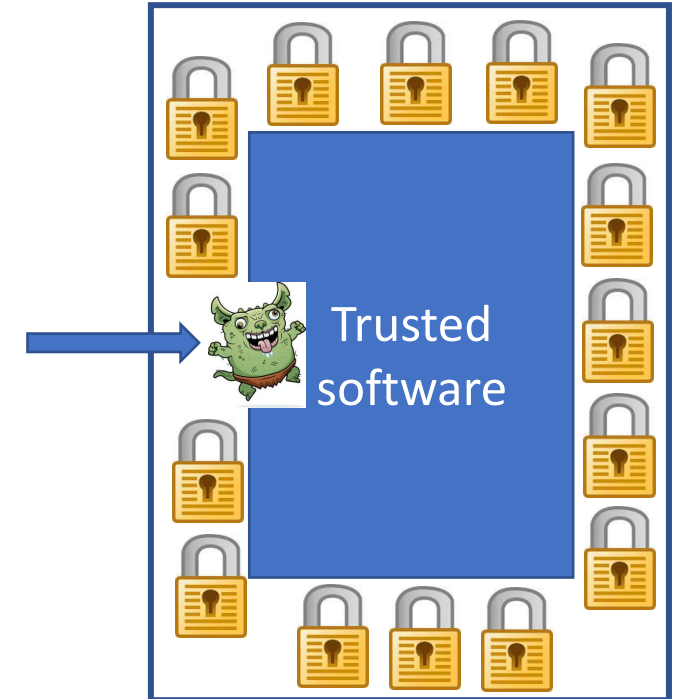
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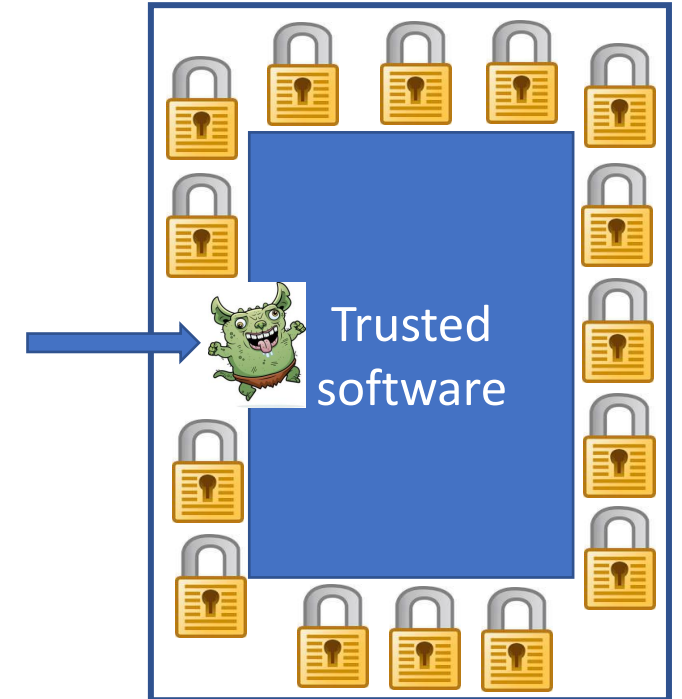
- **TEEs, vulnerable**

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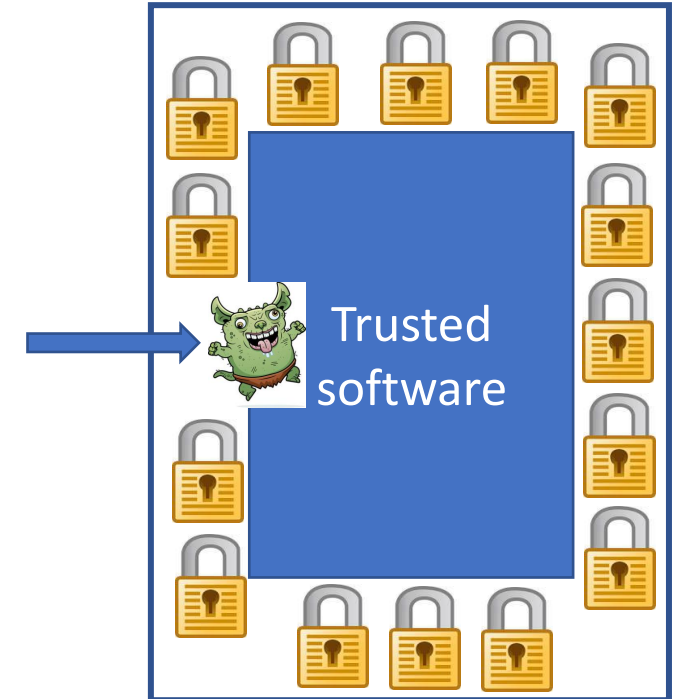
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- **Using Capability architectures**
 - **CHERI** may **mitigate** against a variety of **memory vulnerability** problems.
 - **CHERI** supports the creation of **isolated regions of memory**.



TEE - Trusted Execution Environment and Capabilities

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 - **CHERI** may **mitigate** against a variety of **memory vulnerability** problems.
 - **CHERI** supports the creation of **isolated regions of memory**.
- **Explore CHERI for TEEs**
 - Improve security



Explore CHERI for TEEs

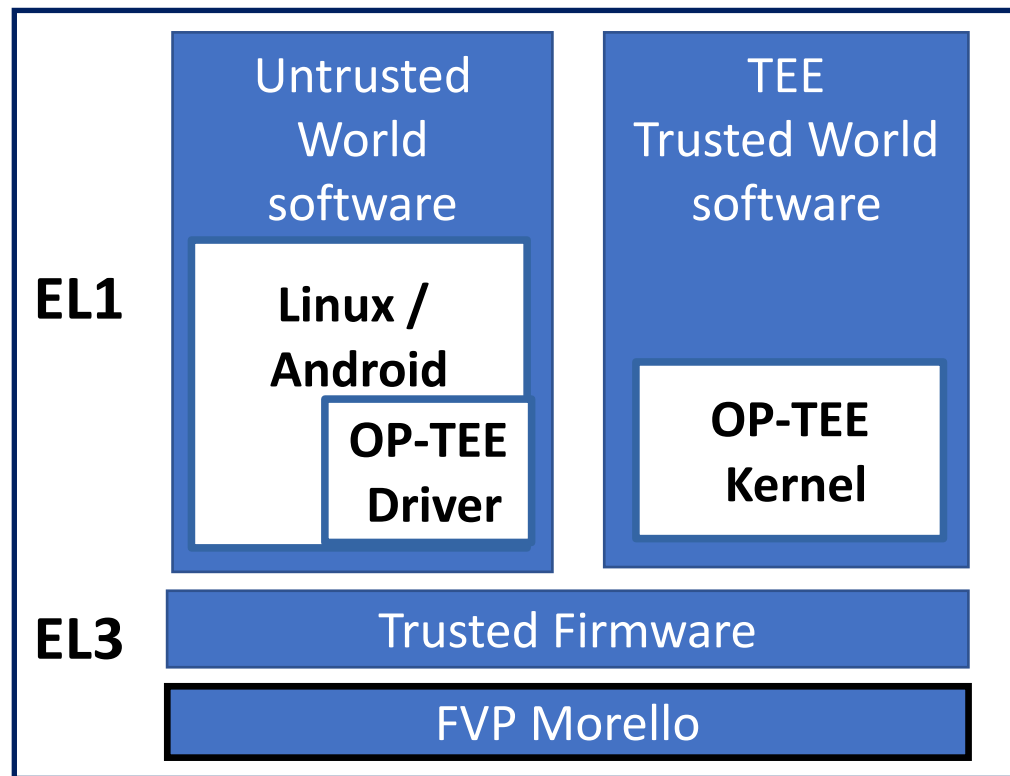
1. CHERI - OPTEE

2. CHERI prototype TEE

Explore CHERI for TEEs

1. CHERI - OPTEE

- Port **existing TEE** runtime such as **OP-TEE**, to a capability enabled TEE.

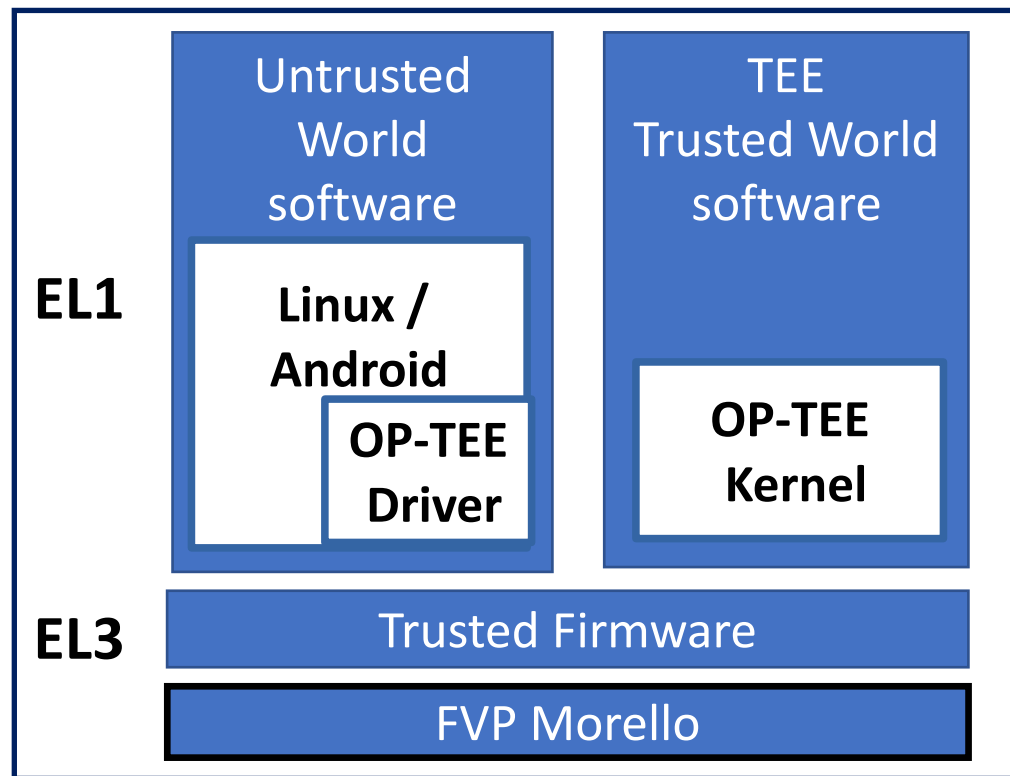


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Explore CHERI for TEEs

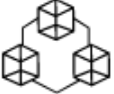
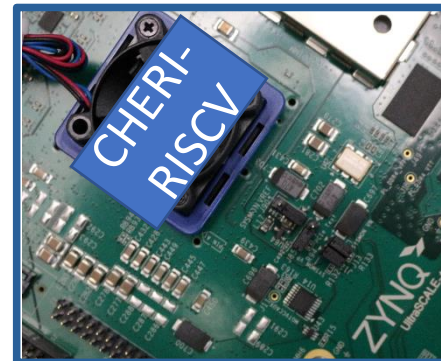
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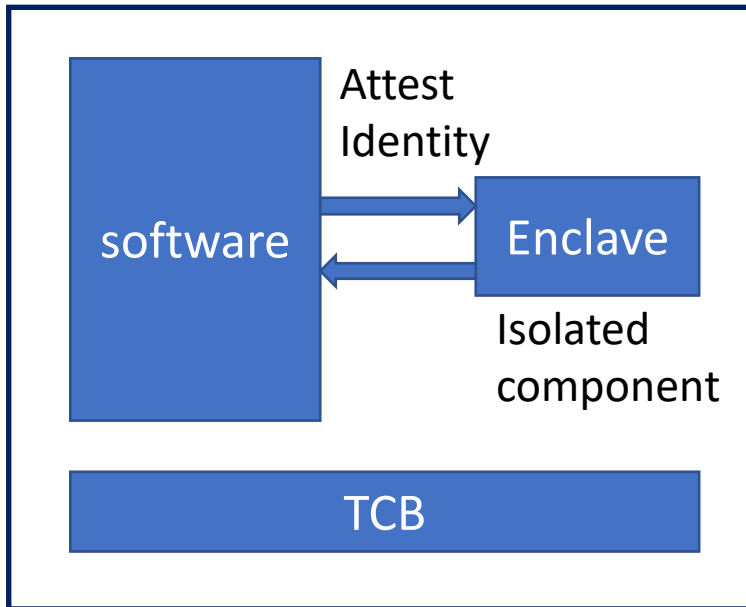
- In collaboration with **KU Leuven**
- prototype **CHERI-TEE on RISC-V**
- prototype **CHERI-TEE on Morello & integration with Trustzone**



Morello Platform Model

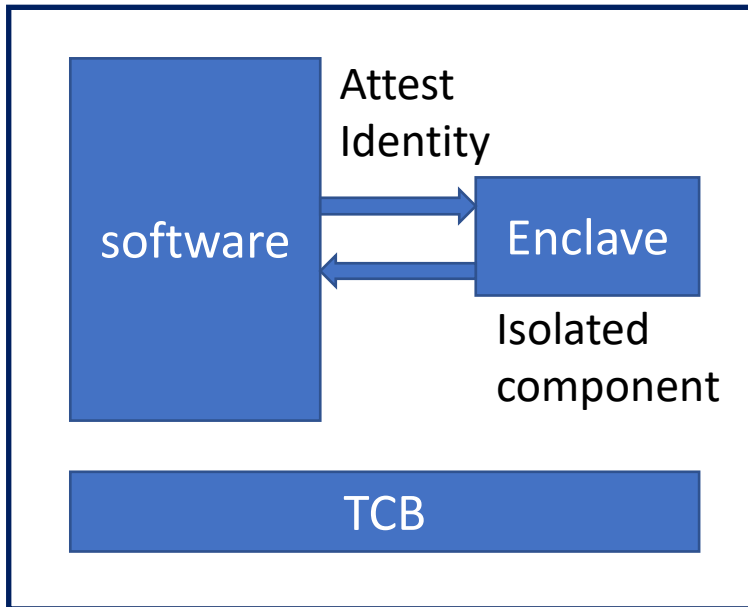
Download the open access Fixed Virtual Platform (FVP) on developer.

CHERI-based Prototype TEE



- Isolated regions of memory
- Created during run-time
- Small trusted computing Base TCB

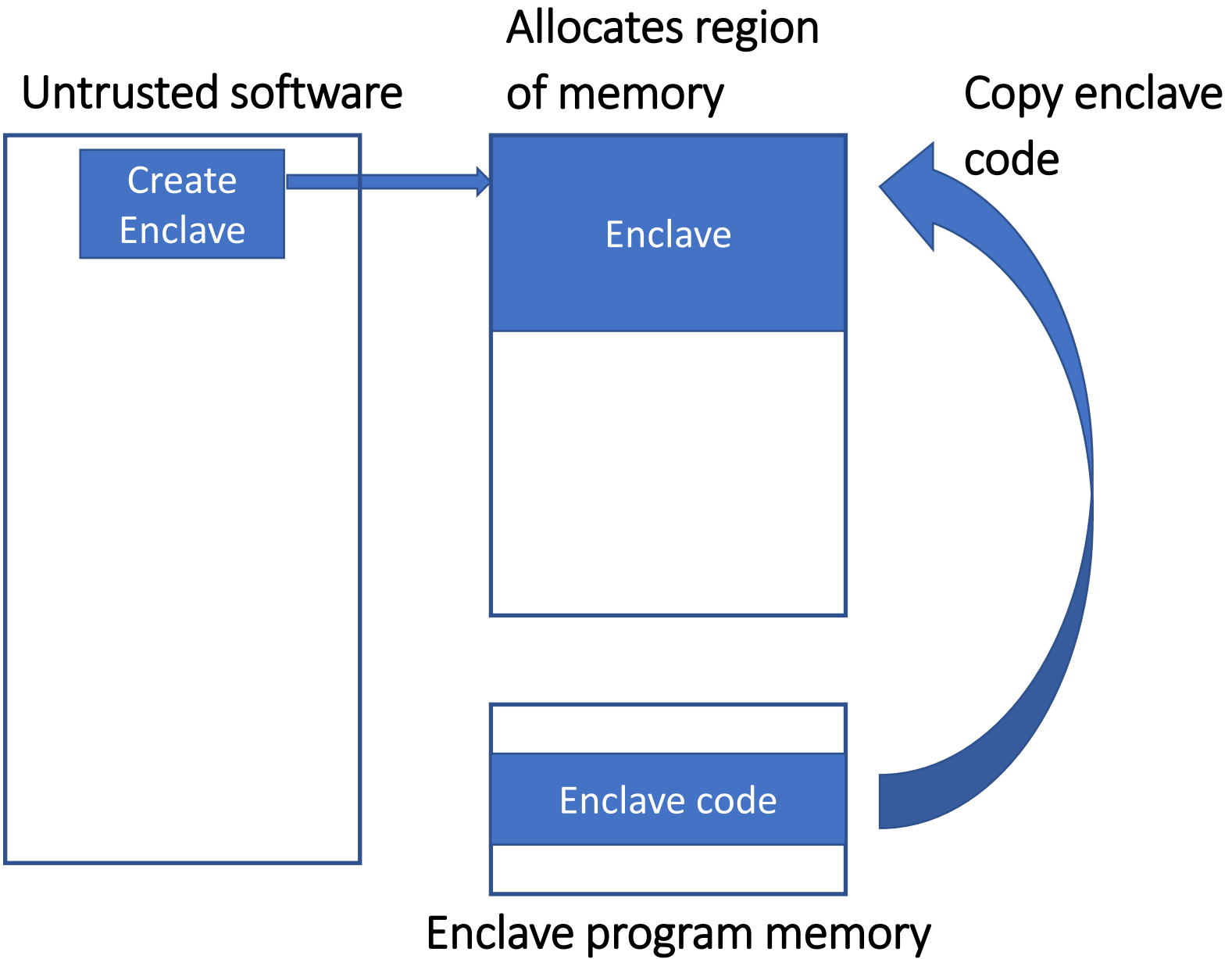
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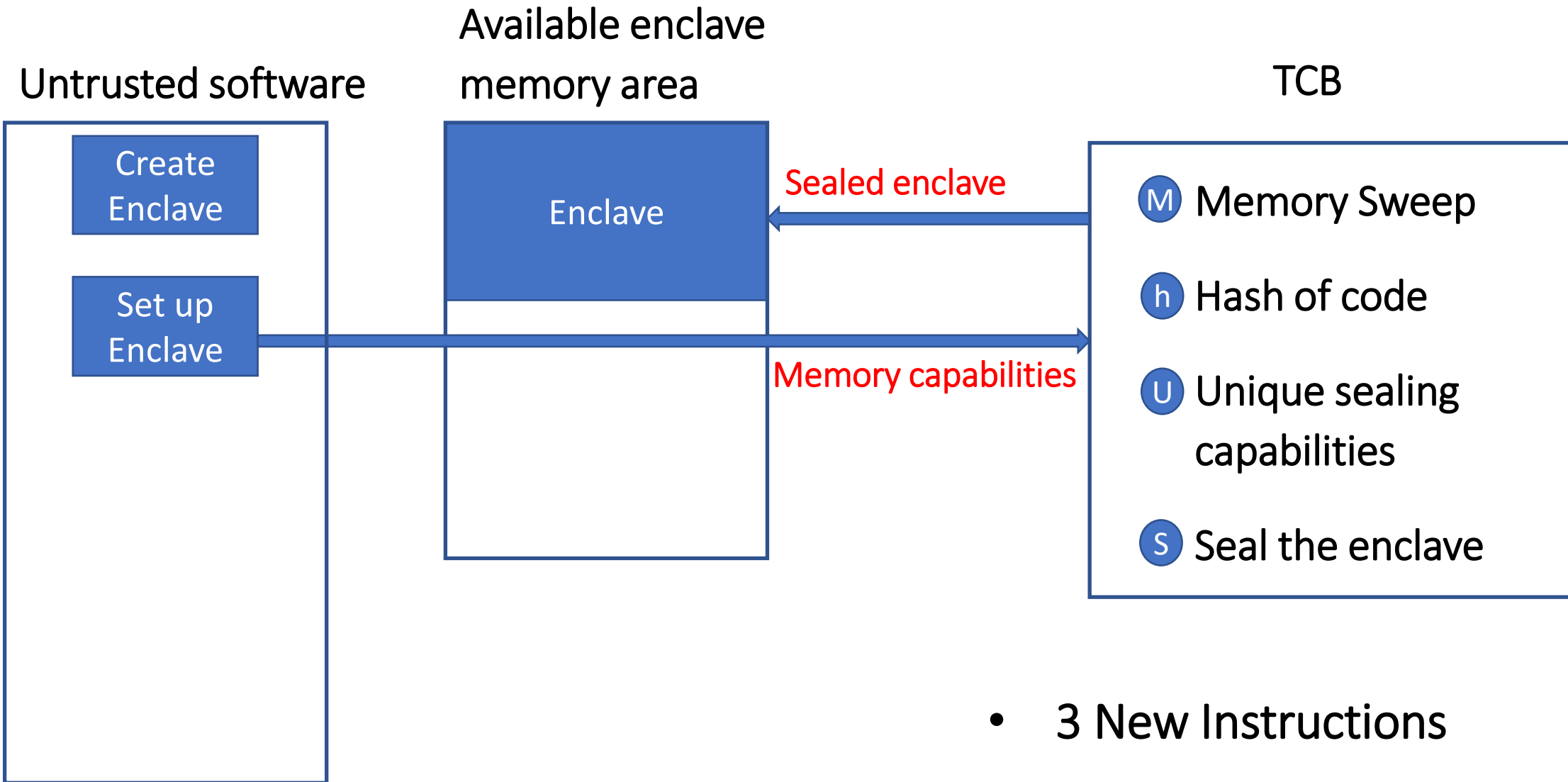


- Isolated regions of memory
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- **Existing CHERI mechanisms**
 - Sealed capabilities pairs
- **Two new mechanisms**
 - **To establish trust in an enclave**
 - **exclusive ownership** of a block of **memory** (memory sweep)
 - **Obtaining sealing capabilities** derived from an enclave's identity (symbolic private key to encrypt or sign other capabilities)
- **First on a capability-based platform**

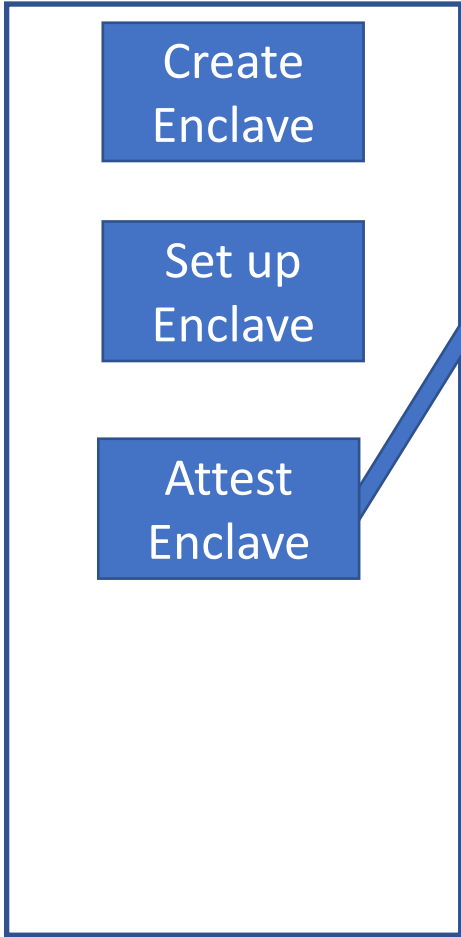
Thomas Van Strydonck, June 2022, PhD Thesis
Formal Reasoning about Hardware Capability Architectures
Chapter 4: CHERI-TrEE: Flexible enclaves on capability machines
<https://lirias.kuleuven.be/retrieve/667137>



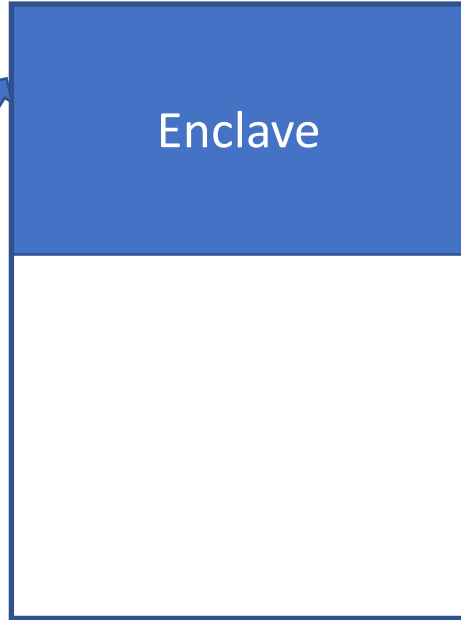


- 3 New Instructions

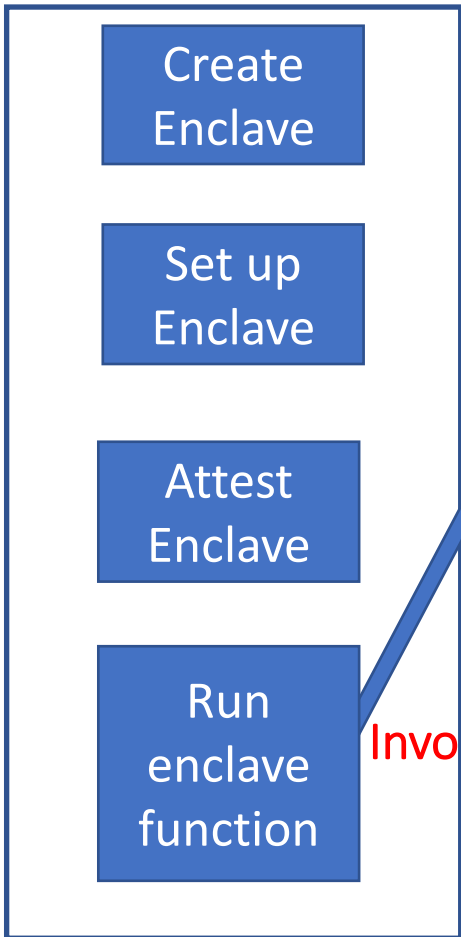
Untrusted software



Available enclave memory area

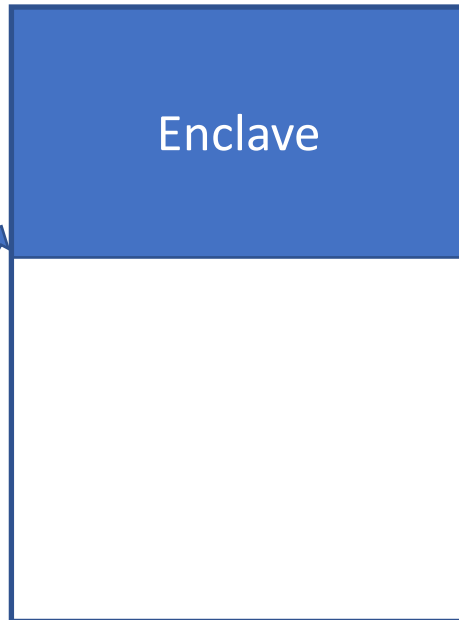


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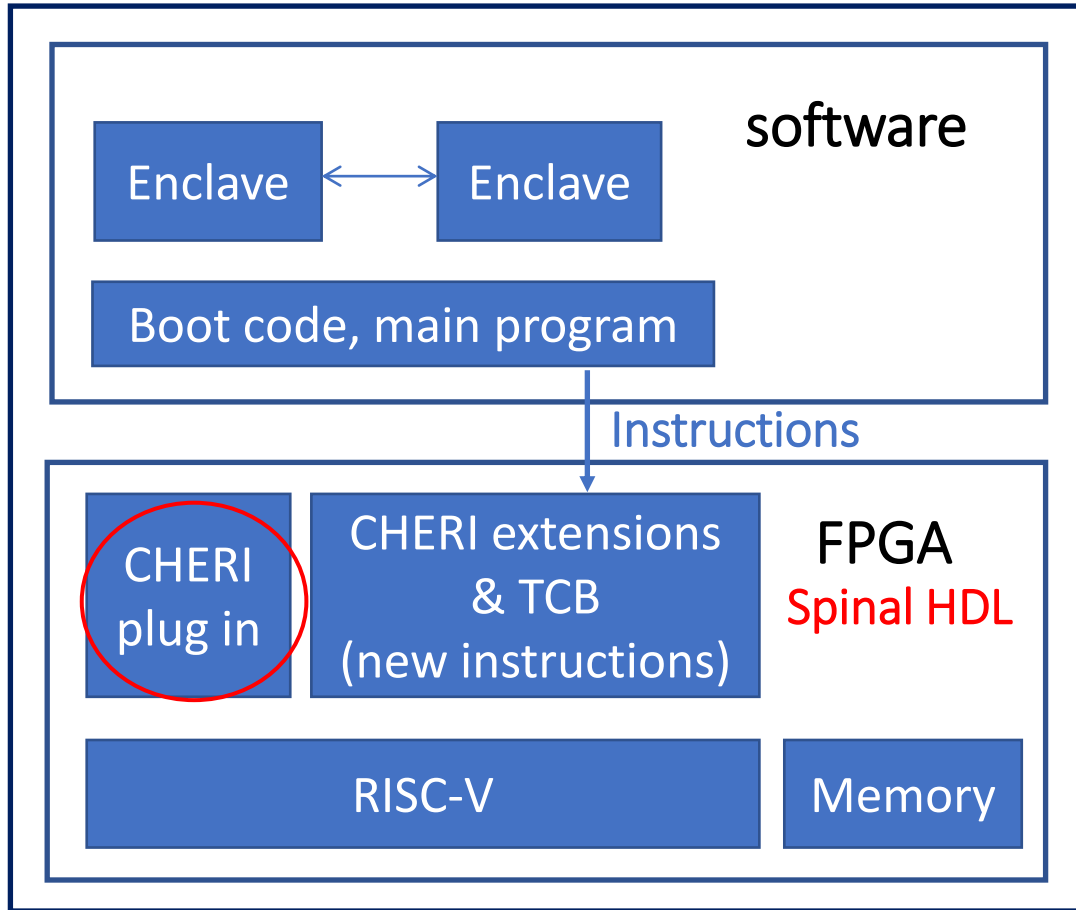
Invoked

Available enclave memory area



- Sealing capabilities to Encrypt / sign results

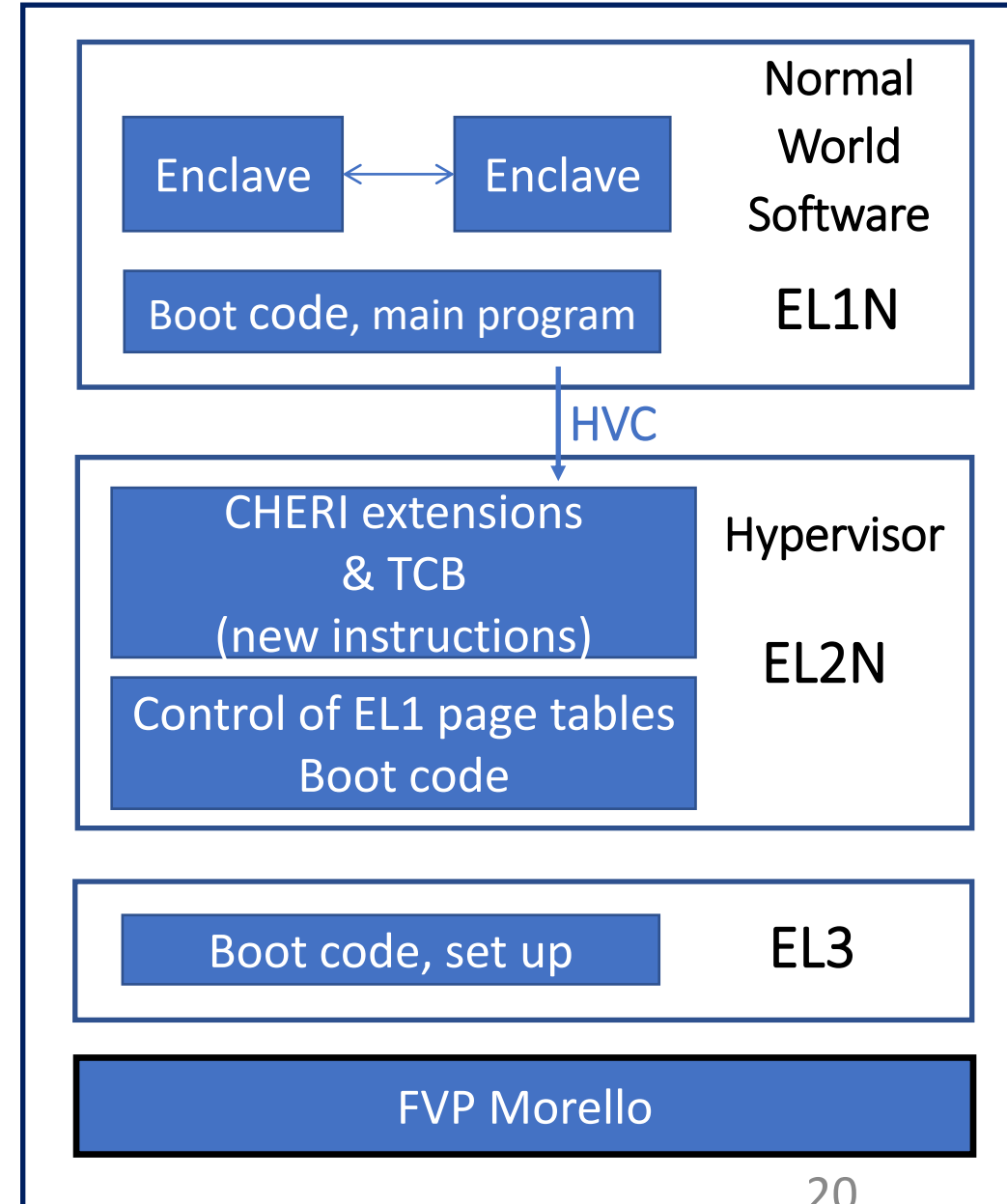
RISC-V Prototype



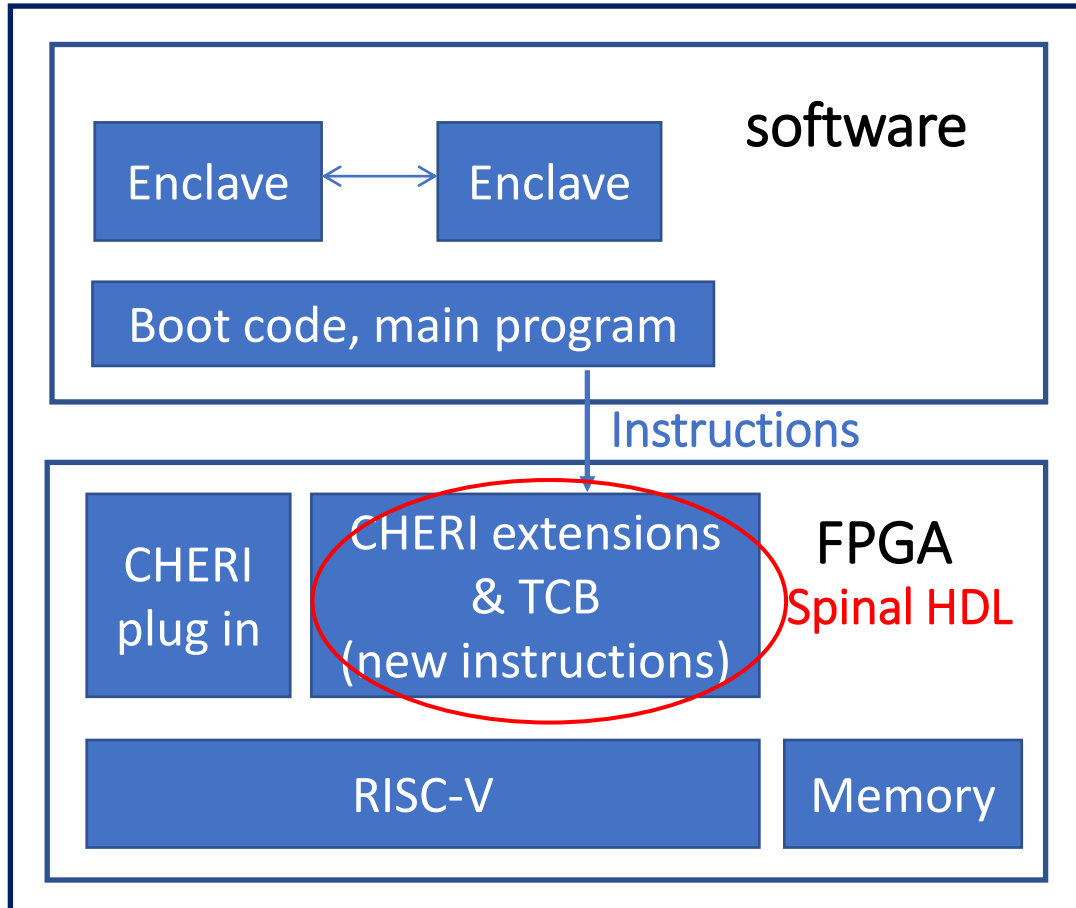
User space enclaves

- In both RISC-V and Morello
- Additional CHERI instructions needed to initialise the enclaves

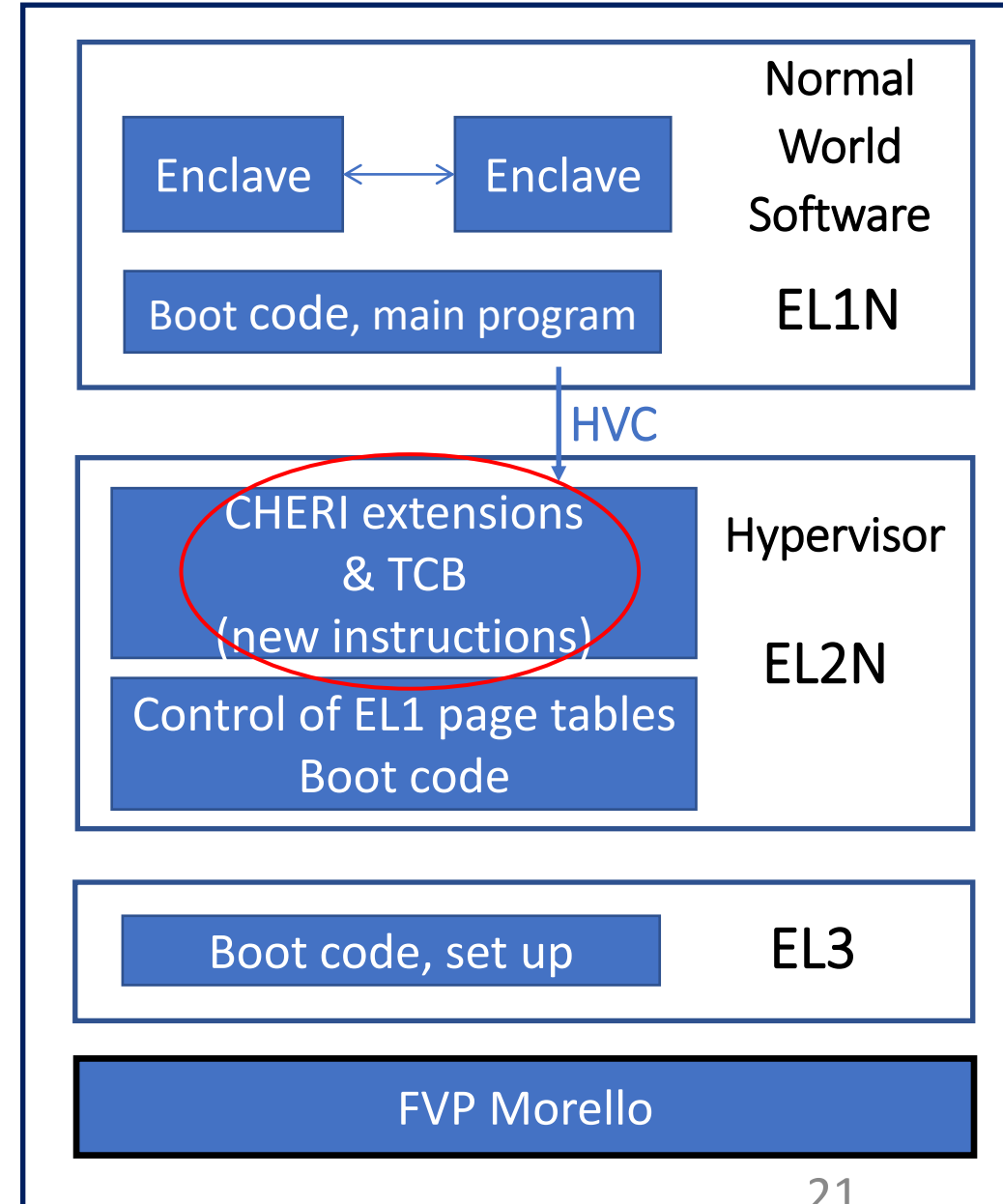
Morello Prototype



RISC-V Prototype



Morello Prototype



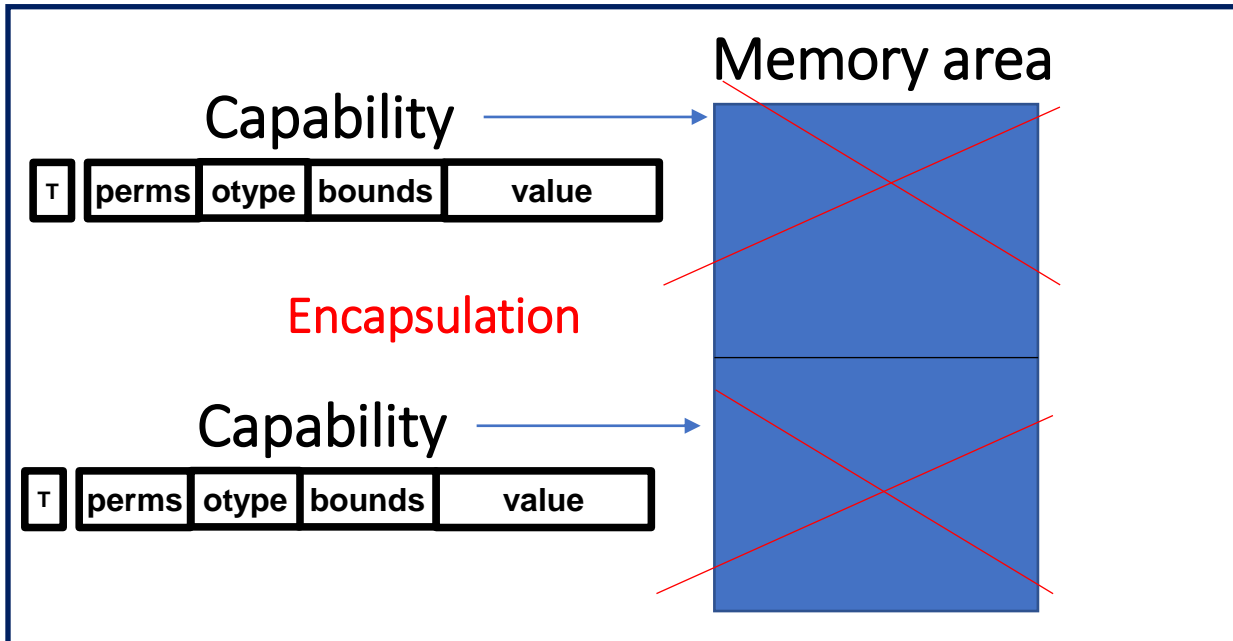
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Object Capabilities and domain switching

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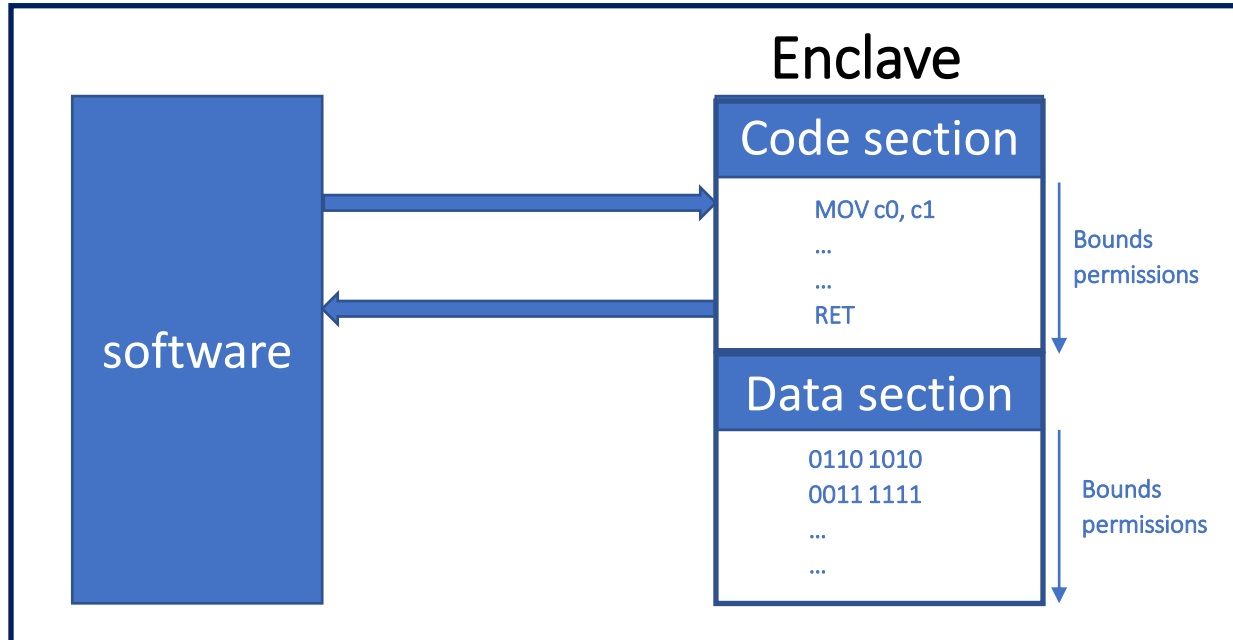
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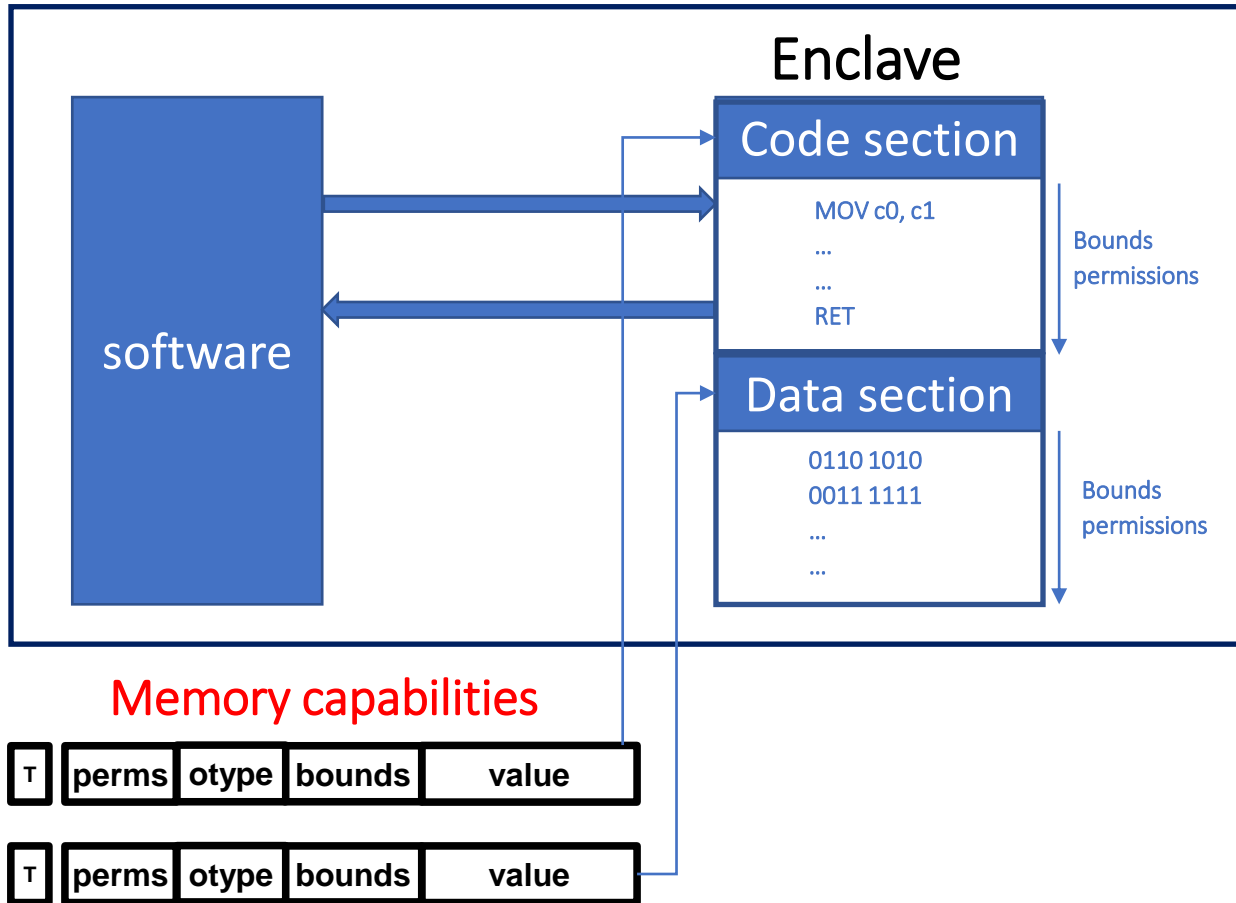
Untrusted access point

Object Capabilities and domain switching



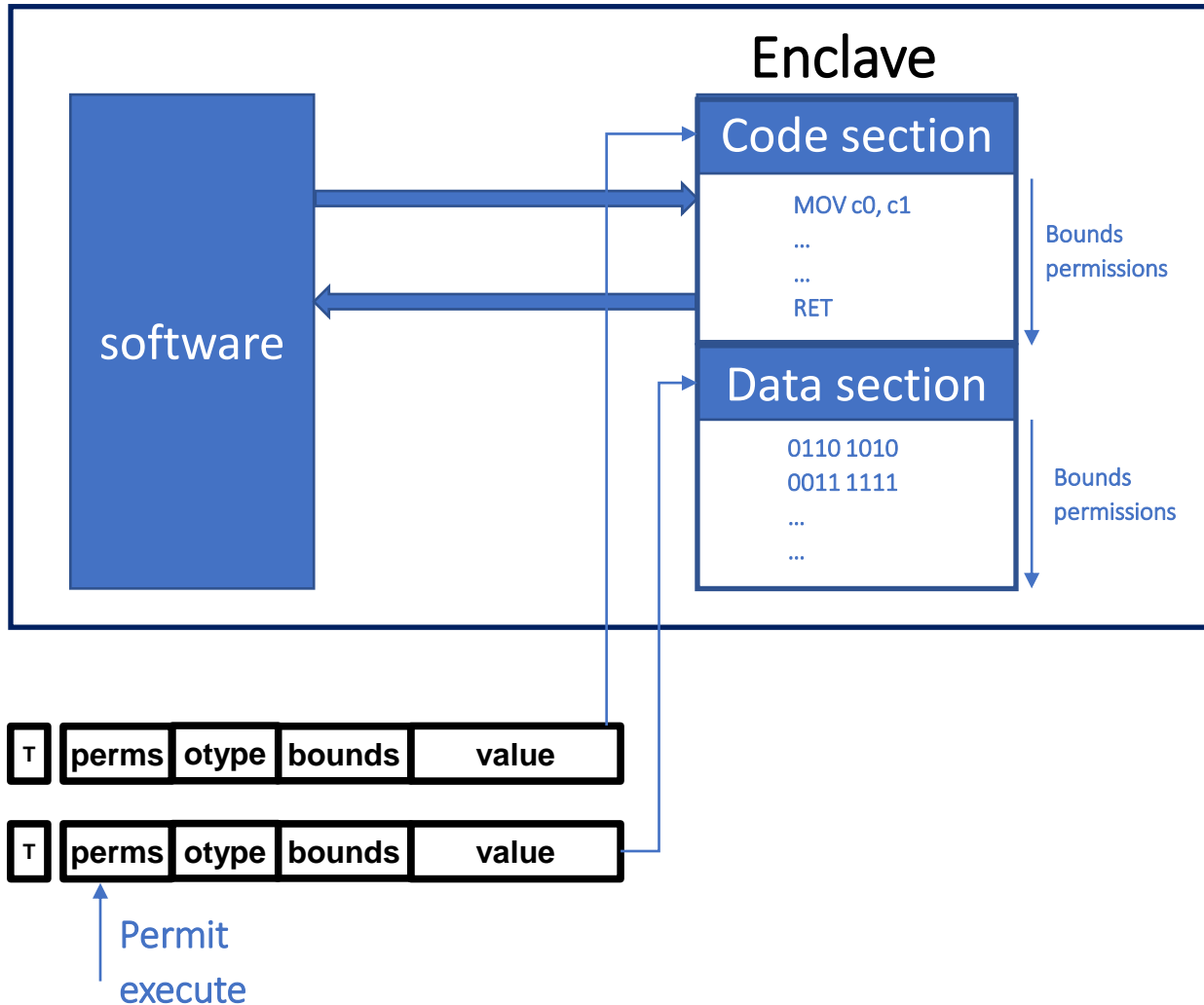
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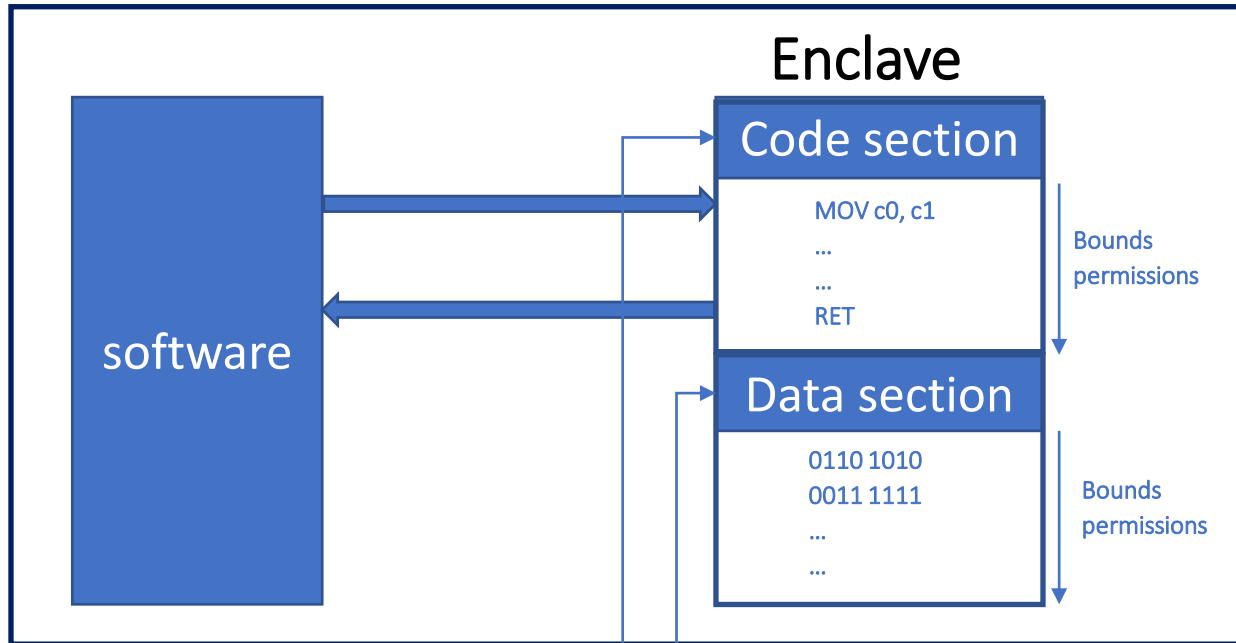
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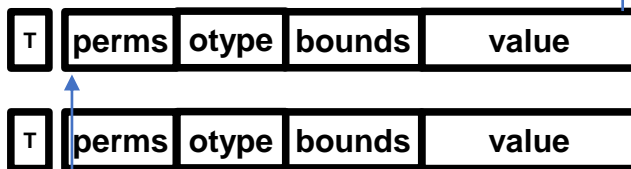


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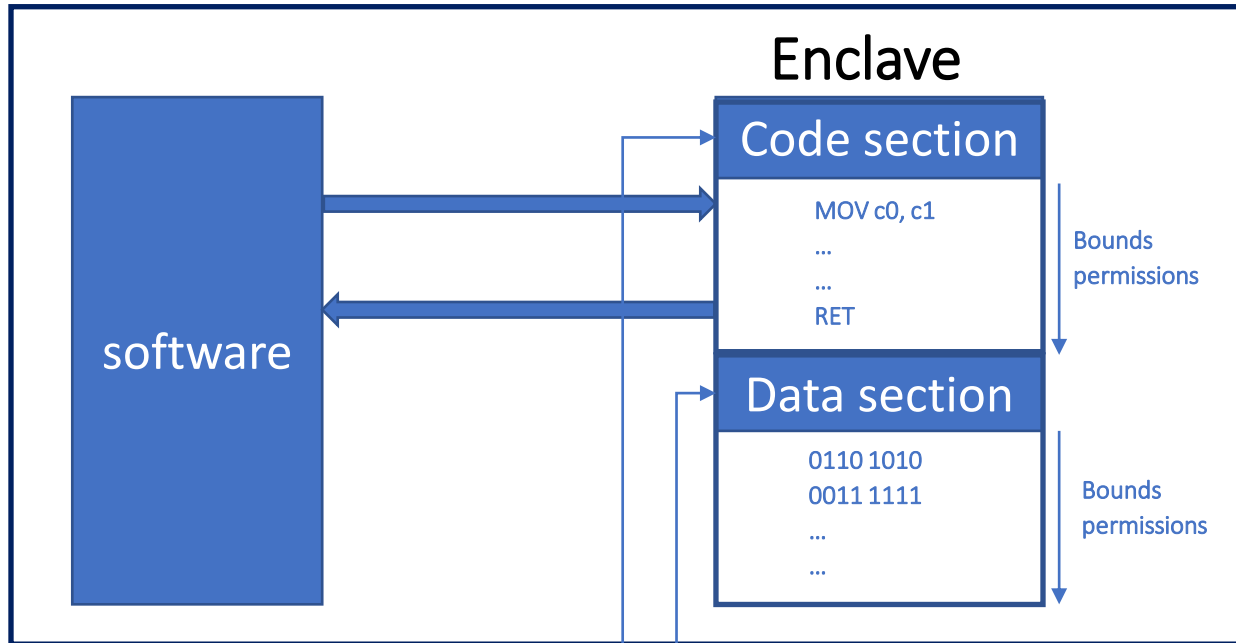


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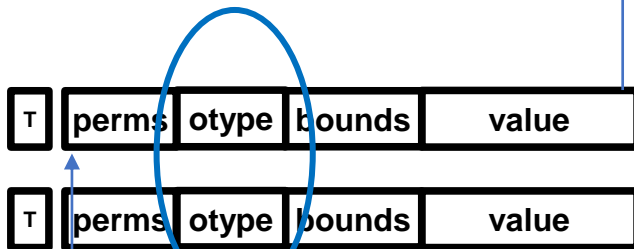


Permit
execute
Permit
Invoke

Object Capabilities and domain switching

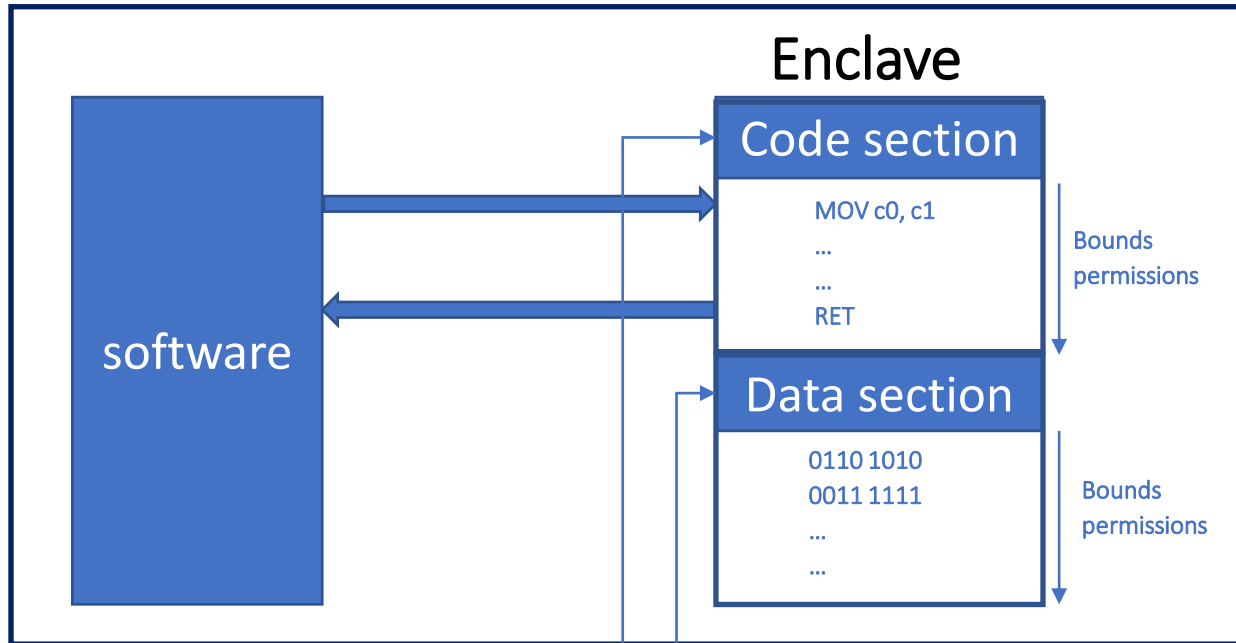


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- Uniquely pair capabilities

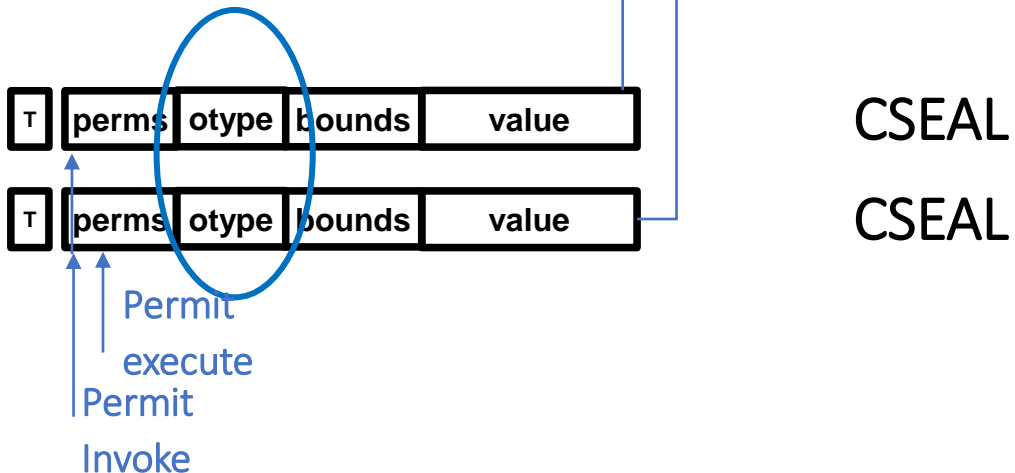


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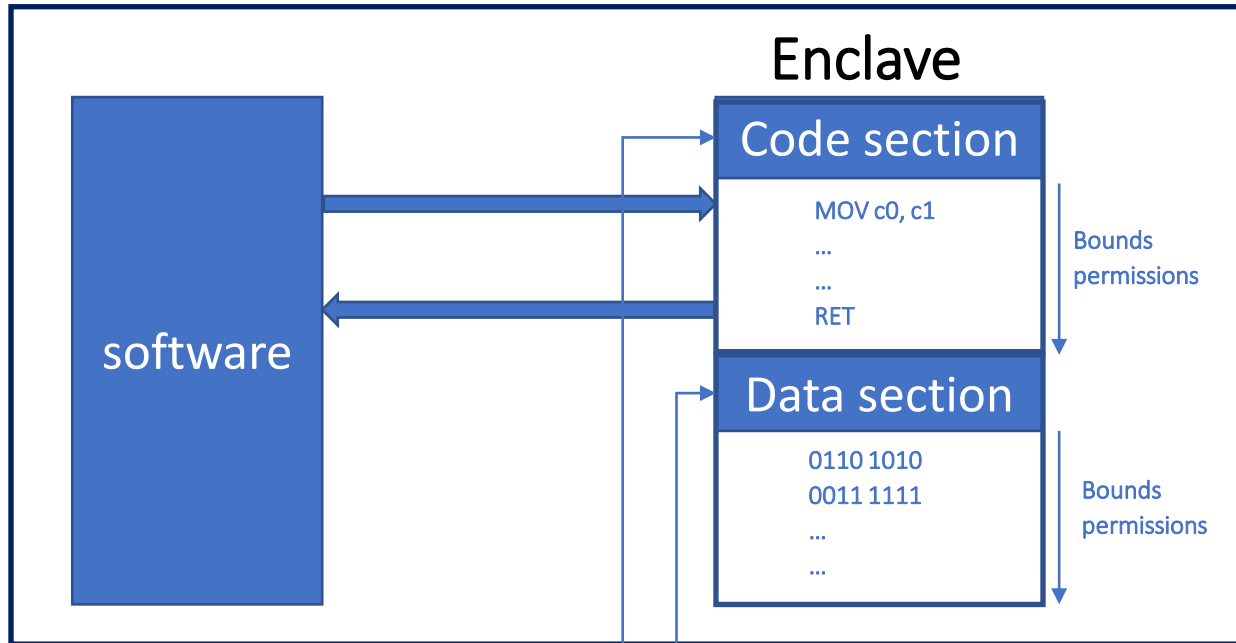
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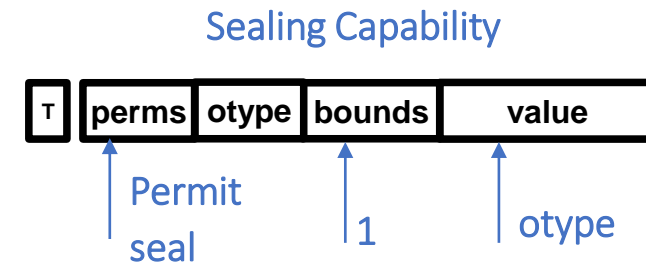
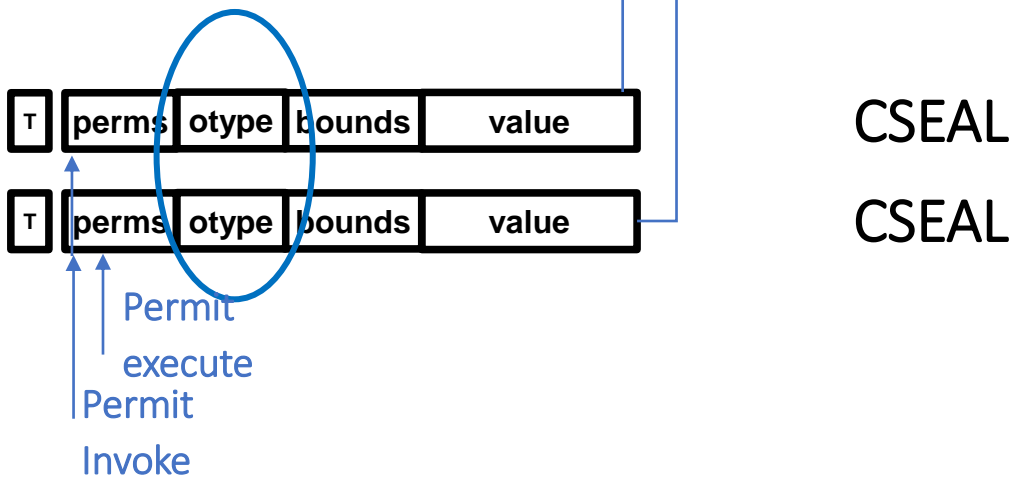
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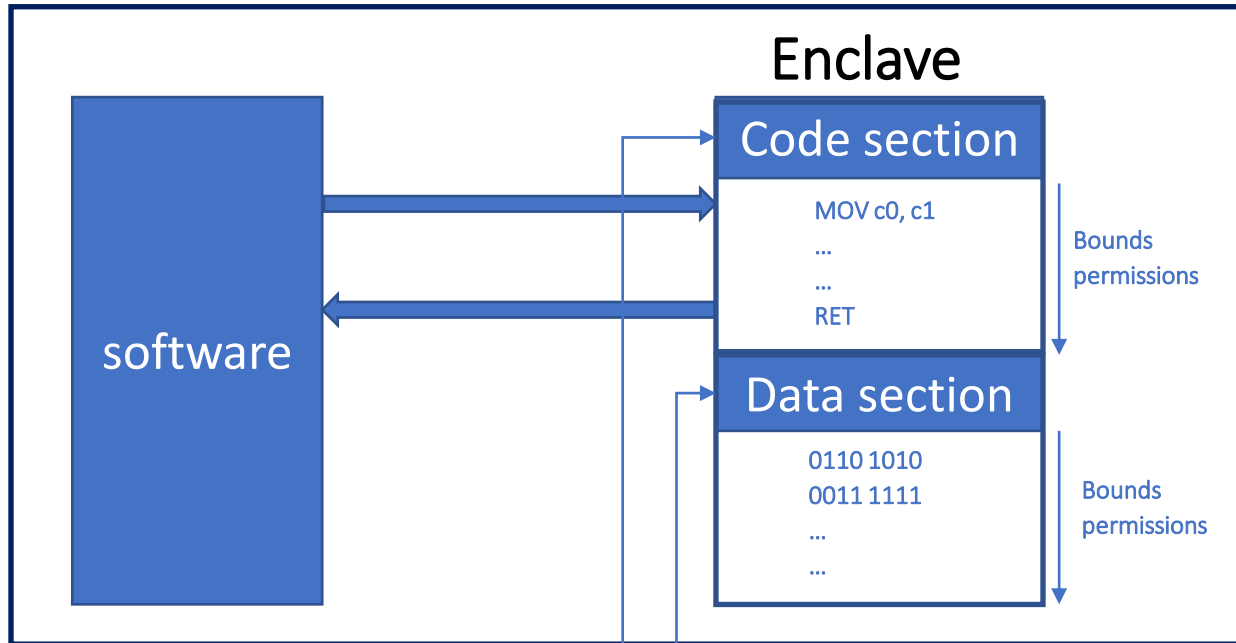
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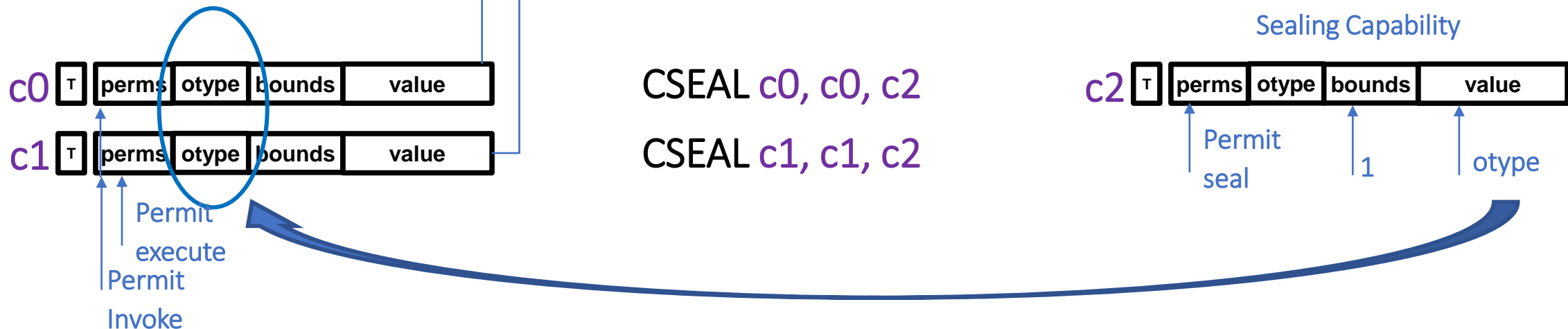
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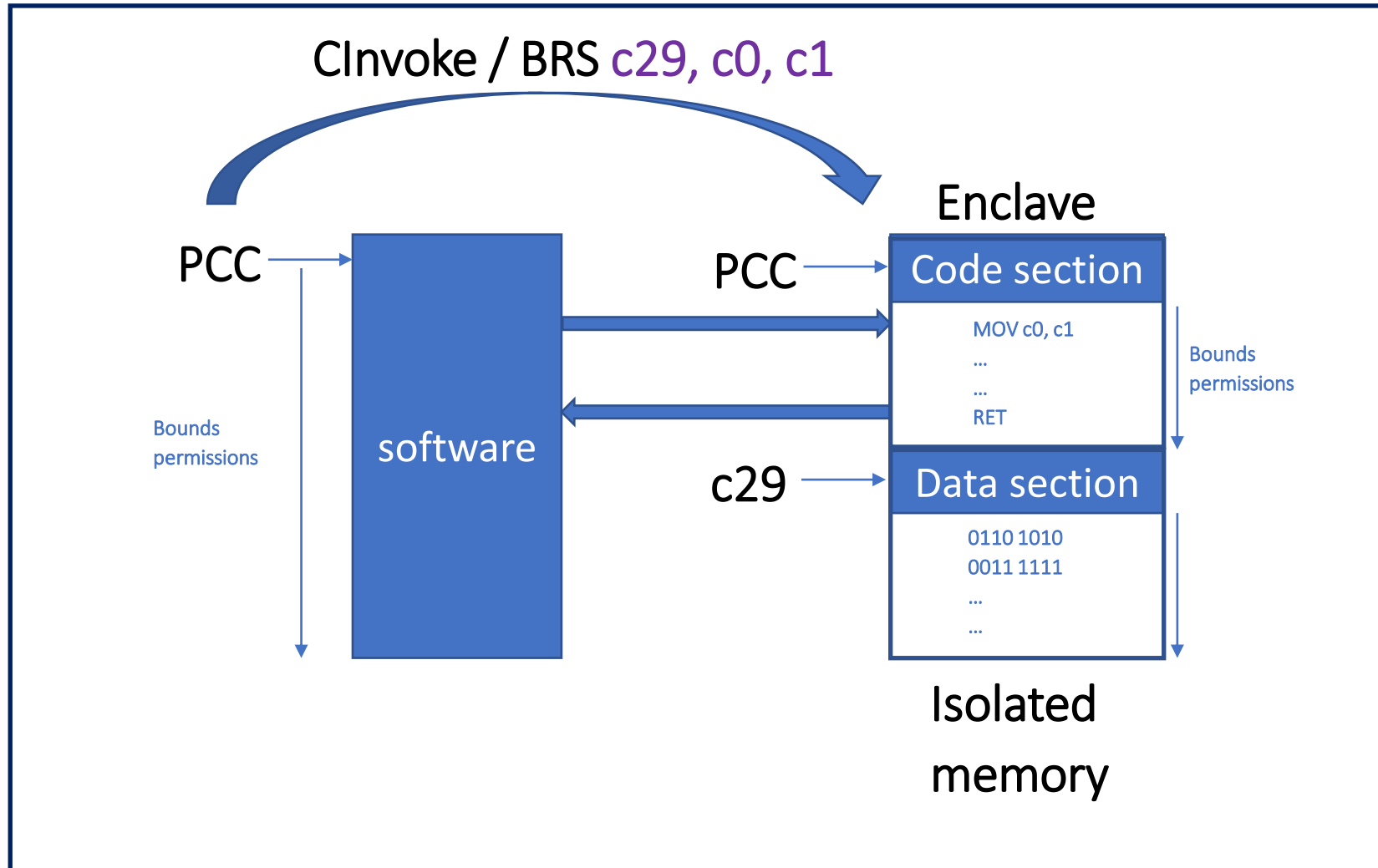
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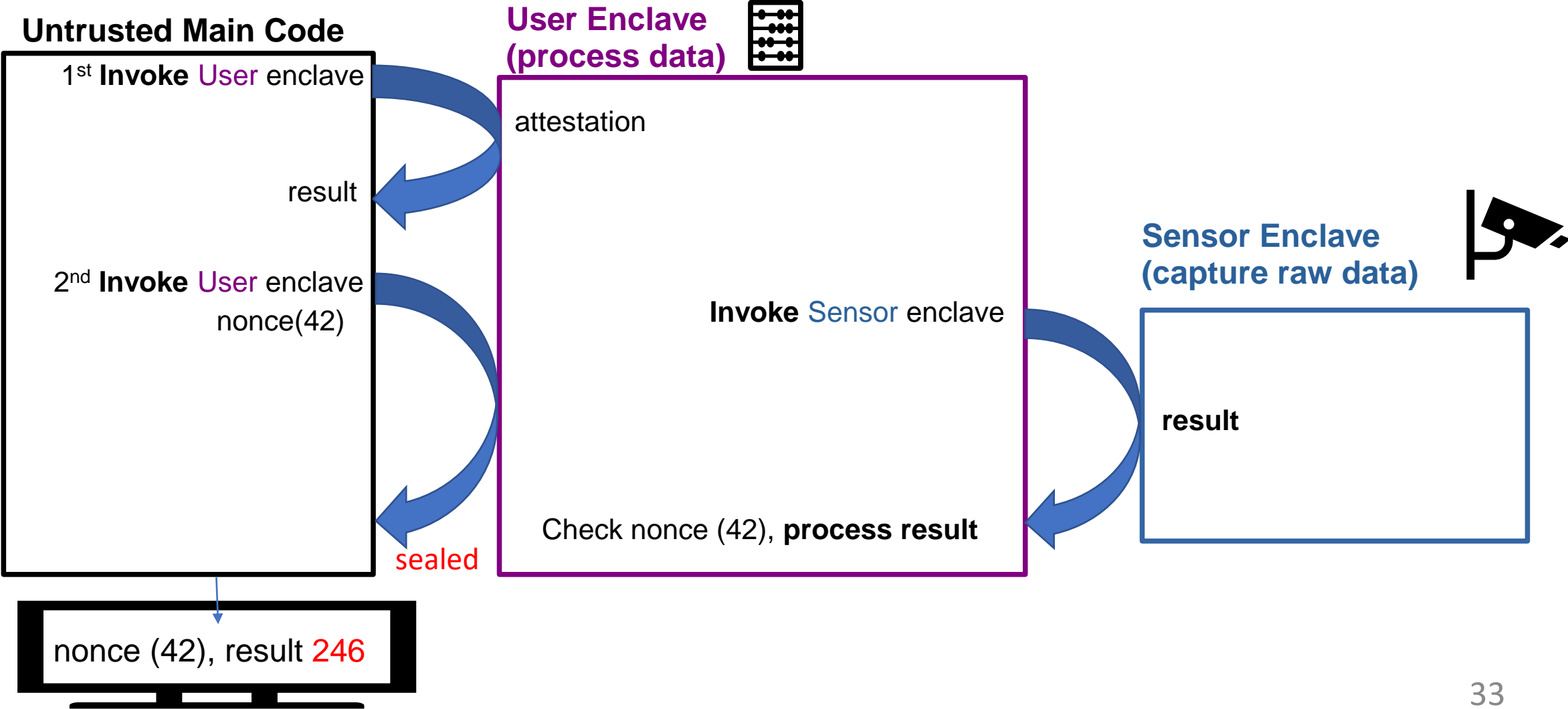


Only way to access
Enclave

Code & data
Unsealed

Run trusted code

Demo and data processing scenario



Demo and data processing scenario

```
FVP terminal_uart0_board
Trying 127.0.0.1...
Connected to localhost.
Escape character is '^]'.
EL3
EL2N
EL1N

capattest demo on Morello

loading sensor enclave.....
sensor_code start: c0100000 end: c0100180
```

End